

## Modeling of Canonical Switching Cell Converter Using Genetic Algorithm

T. V. Viknesh<sup>1</sup> and V. Manikandan

**Abstract :** The working of Canonical switching cell (CSC) converter was studied and its equivalent circuit during ON and OFF states were obtained. State space model of CSC converter in ON and OFF states were developed using the Kirchhoff laws. The state space matrices were used to construct the transfer functions of ON & OFF states. The step response of the converter was simulated using MATLAB. The step response curve was obtained using different values of circuit components ( $L$ ,  $C_1$ ,  $C_2$  and  $R_L$ ) and optimized. The characteristic parameters such as rise time, overshoot, settling time, steady state error and stability were determined using the step response curve. The response curve shows that there is no overshoot; the rise time and settling time are very low as expected for a converter and its stability is very high but the amplitude is very. The circuit was tuned to attain the expected amplitude using PID controller with the help of Genetic algorithm. The excellent results of circuits' characteristic parameters are very useful guideline for constructing such CSC converters for DC-DC conversions. The circuit characteristic parameters are useful in constructing such CSC converters for DC-DC conversions in driving solar energy using solar panel.

**Keywords:** Canonical switching cell converter, state-space methods, DC-DC converter, step response, stability, power system modeling, switching circuits, genetic algorithm, PID.

### 1 Introduction

In all the domestic, commercial and military applications, power electronic converters are essential elements in utilization part of power network. Due to the higher efficiency, light weight, and low cost, the switching DC-DC converters have generated a significant research in area of modeling, analysis and control. The building blocks of the converters are inductors, capacitors, resistor and switching elements [VashistBist, BhimSingh (2014); Divya Navamani Jayachandran, Vijayakumar Krishnaswamy, Lavanya Anbazhagan and Karthikeyan Dhandapani (2015); C.T. Rim, G.B. Joung, G.H. Cho (1988)]. These converters must be examined and investigated in terms of their component relationships and topographical characteristics to design and develop well suited converter for our

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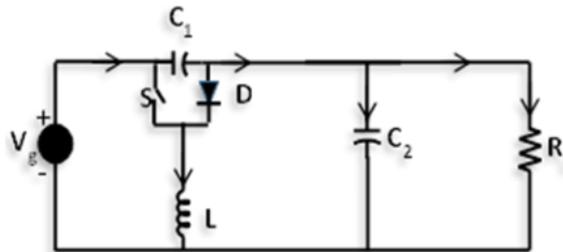
required applications. The canonical converters are playing vital role in DC-DC (boost-buck) conversion in solar panel charge controllers.

### ***1.1 State space model***

Many dynamic systems of engineering, biological, social and economic systems can be easily modeled and completely characterized using state space modeling technique. To analyze any system, it is essential to reduce the complexity of the mathematical expressions, as well as to resort to computers for most of the tedious computations necessary in the analysis; state-space approach is best suited for this purpose. Many state space models are used widely to study static and dynamic characteristics of converter as well as to design their control system to achieve specific regulation characteristics [Mohammad Reza Modabbernia, Ali Reza Sahab, Mohammad Taghy Mirzaee, Kazem Ghorbany (2012)]. State space concepts provide the access to what is going inside the system in addition to input and output mapping. State space modeling is a mathematical model of a physical system as set of input, output and set of variables related by first order differential equation. State space refers to Euclidean space in which variables on axes are state variables. The state of the system can be represented as vector within space. In the system (circuit), the number of state variables is equal to number of independent energy storage elements in it. The dynamic systems are linear, time variant and finite dimensional, then the differential and algebraic equation are written in matrix form. The time domain representation provides convenient and compact way to model and analyze the systems with multiple input and outputs [Preiti Tyagi, V. C. Kotak, B. Mathew, V. P. Sunder Singh (2014); J. C. Mayo-Maldonado, R. Salas-Cabrera, H. Cisneros-Villegas, M. Gomez-Garcia, E. N. Salas Cabrera, R. Castillo-Gutierrez and O. Ruiz-Martinez (2010); J.C.Mayo- Maldonado, R. Salas-Cabrera, J.C. Rosas-Cario, J. De Leon- Morales, and E. N. Salas- Cabrera (2011)].

### ***1.2 Canonical switching cell (CSC) converter***

CSC converter is a buck-boost DC-DC converter and it has less number of devices, such as inductor, resistor, capacitor, diode and switch as shown in fig.1. It works mainly in two different modes as i) switch on mode and ii) switch off mode. There is energy transfer between L and  $C_2$ . The circuit's characteristic parameters are useful in constructing such CSC converters for DC-DC conversions.



**Figure 1:** Canonical switching cell converter

## 2 State space modeling of CSC

### 2.1 Five stages of work

- 1) The various components and their parameters in the circuit were identified and classified as constant parameters, input, state variable and output parameters of CSC.
- 2) The state space equations of the CSC during ON and OFF states were obtained by using Kirchhoff's laws. Then the state space matrices for both ON and OFF states were written.
- 3) The step response graphs were simulated using the matrices in Matlab for both ON and OFF states.
- 4) The ON and OFF states transfer functions were obtained by using Matlab.
- 5) Using Simulink, a closed loop block was designed and simulated by using the obtained  $k_p$ ,  $k_i$  and  $k_d$  values till to obtain the perfect response curves the parameters like rise time, settling time, overshoot, and stability were analyzed. In order to obtain the steady state within very short time, the PID tuning technique was introduced using the genetic algorithm.

### 2.2 CSC converter components and modeling parameters

This circuit contains three energy storing devices i) inductor (L), ii) capacitor ( $C_1$ ) and iii) capacitor ( $C_2$ ). The physical quantities of the converter are represented in state space notation as below.

The input, state variable and output quantities were assumed

*Input parameters:*  $V_g$  ( $U_1$ ), and  $i$  ( $U_2$ )

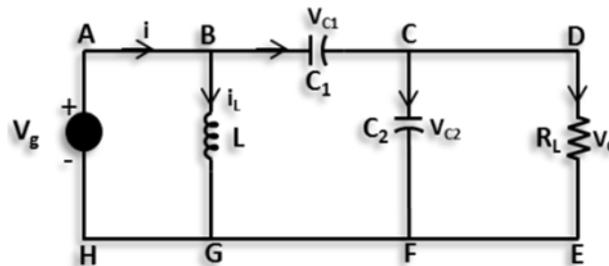
*State variables:*  $i_L$  ( $x_1$ ),  $V_{C1}$  ( $x_2$ ), and  $V_{C2}$  ( $x_3$ )

*Output parameter:*  $V_0$  ( $Y$ )

The other parameters such as L,  $C_1$ ,  $C_2$  and  $R_L$  are considered as constants and to be optimized for the best performance.

### 2.3 State space model for CSC converter in ON state

During the ON state of the switch (S), the equivalent circuit of the converter is as shown in fig.2. The mathematical formulations of the circuit are stated with basic laws. Kirchhoff's current law and Kirchhoff's voltage law are used at the junctions and in the loops of the converter and the corresponding state space equations are obtained as below.



**Figure 2:** Canonical switching cell converter in ON state

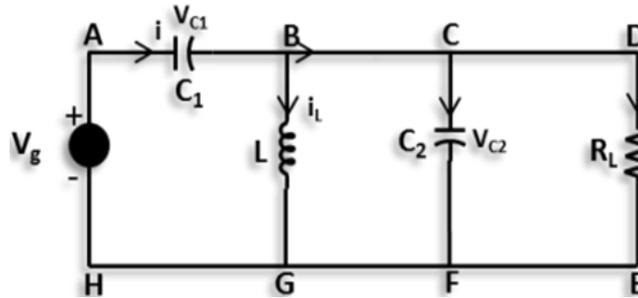
Using the Kirchoff laws in the loop ABGH, and at the junctions B, and C, the following state space equations are obtained (**Annexure I**)

$$\dot{x}_1 = \frac{U_1}{L}, \quad \dot{x}_2 = \frac{U_2}{C_1} - \frac{x_1}{C_1}, \quad \dot{x}_3 = \frac{C_2 \dot{x}_3 + Y}{C_1 R_L}, \quad \dot{x}_3 = \frac{U_2}{C_2} - \frac{x_1}{C_2} - \frac{x_3}{C_2 R_L} \quad \text{and} \quad x_3 = Y$$

$$\begin{bmatrix} \dot{x}_1 \\ \dot{x}_2 \\ \dot{x}_3 \end{bmatrix} = \begin{bmatrix} 0 & 0 & 0 \\ -\frac{1}{C_1} & 0 & 0 \\ -\frac{1}{C_2} & 0 & -\frac{1}{C_2 R_L} \end{bmatrix} \begin{bmatrix} x_1 \\ x_2 \\ x_3 \end{bmatrix} + \begin{bmatrix} \frac{1}{L} & 0 \\ 0 & \frac{1}{C_1} \\ 0 & \frac{1}{C_2} \end{bmatrix} \begin{bmatrix} U_1 \\ U_2 \end{bmatrix} \quad \text{and} \quad [Y] = [0 \ 0 \ 1] \begin{bmatrix} x_1 \\ x_2 \\ x_3 \end{bmatrix} + [0 \ 0] \begin{bmatrix} U_1 \\ U_2 \end{bmatrix}$$

#### 2.4 State space model for CSC converter in OFF state

During the OFF state of the switch (S), the equivalent circuit of the converter is as shown in fig.3. Similar to ON state, mathematical formulations of the circuit are stated with basic laws. Kirchoff's current law and Kirchoff's voltage law are used at the junctions and in the loops ABGH and CDEF and at the junction B of the converter and the corresponding state space equations are obtained as below. (**Annexure II**)



**Figure 3:** Canonical switching cell converter in OFF state

$$\dot{x}_1 = \frac{U_1}{L} - \frac{x_2}{L}, \quad \dot{x}_2 = \frac{U_2}{C_1}, \quad \dot{x}_3 = \frac{U_2}{C_2} - \frac{x_1}{C_2} - \frac{x_3}{C_2 R_L}, \quad \text{and} \quad x_3 = Y$$

$$\begin{bmatrix} \dot{x}_1 \\ \dot{x}_2 \\ \dot{x}_3 \end{bmatrix} = \begin{bmatrix} 0 & -\frac{1}{L} & 0 \\ 0 & 0 & 0 \\ -\frac{1}{C_2} & 0 & -\frac{1}{C_2 R_L} \end{bmatrix} \begin{bmatrix} x_1 \\ x_2 \\ x_3 \end{bmatrix} + \begin{bmatrix} \frac{1}{L} & 0 \\ 0 & \frac{1}{C_1} \\ 0 & \frac{1}{C_2} \end{bmatrix} \begin{bmatrix} U_1 \\ U_2 \end{bmatrix} \quad \text{and} \quad [Y] = [0 \ 0 \ 1] \begin{bmatrix} x_1 \\ x_2 \\ x_3 \end{bmatrix} + [0 \ 0] \begin{bmatrix} U_1 \\ U_2 \end{bmatrix}$$

### 3 Simulation details

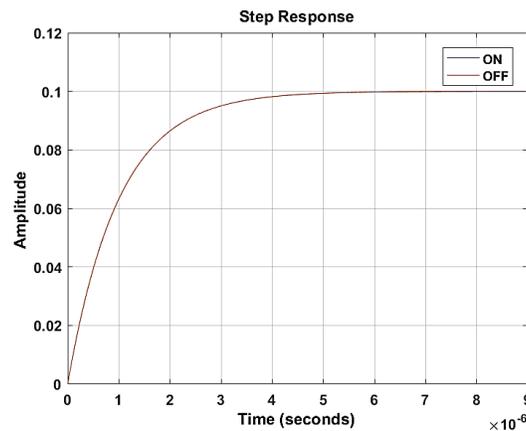
The above obtained state space model of the CSC was simulated using MATLAB. The simulation work was executed using the software MATLAB 2016a in a Pentium 4 core i6 computer. The step response graphs were simulated for both ON and OFF states. Then the ON and OFF states transfer functions were obtained then PID controller with Genetic Algorithm tool, the response curves were simulated and the obtained  $k_p$ ,  $k_i$  and  $k_d$  values were noted. Using Simulink, a blank model was taken and a closed loop block was designed and simulated using the obtained  $k_p$ ,  $k_i$  and  $k_d$  values till to obtain the perfect response curves.

The value of the circuit elements and other input and output parameters of the converter are evaluated from the following ways. The input and output voltages, switching frequency, and efficiency are taken as 12 V, 12 V, 200 KHz and 99 % respectively. The constants such as the value of load resistor( $R_L$ ), inductor (L)and capacitors ( $C_1$ and  $C_2$ ) are assumed in the range of 0.1 to 100  $\Omega$ ,  $5 \times 10^{-3}$  to  $50 \times 10^{-3}$ H, 10  $\mu$ F and 1 to 100 $\mu$ F

### 4 Simulation results

#### 4.1 Step response analysis

The performance of the CSC was studied using the step response curve simulated using Matlab for both ON and OFF states using state space matrices using the below mentioned MATLAB code. The various values of L,  $C_1$ ,  $C_2$  and  $R_L$  were included in the space matrices and their step responses were studied using the step response graph. The optimized values of Load resistance ( $R_L$ ) Inductor (L) Capacitors ( $C_1$  and  $C_2$ ) are  $\leq 1\Omega$ , 5mH, 10  $\mu$ F and 10  $\mu$ F respectively.



**Figure 4:** Step response of the CSC converter during ON and OFF states

MATLAB simulation work was executed using the software MATLAB 2016a with a Pentium 4 i6 computer, the step response graphs were obtained for both ON and OFF states as shown in figure 5. The graphs are exactly like each other and their values are lying within few nano seconds, the graphs show that they are highly stable in nature. The characteristic parameters such as rise time, overshoot, settling time, steady state error and

stability are determined. The rise time and settling time are  $4 \times 10^{-8}$  s and  $8 \times 10^{-8}$  s respectively but the amplitude is 0.1 instead 1. The optimization tasks were analyzed and then PID tuning technique with genetic algorithm was chosen and incorporated as follows. The response curve shows no overshoot and very high stable in nature.

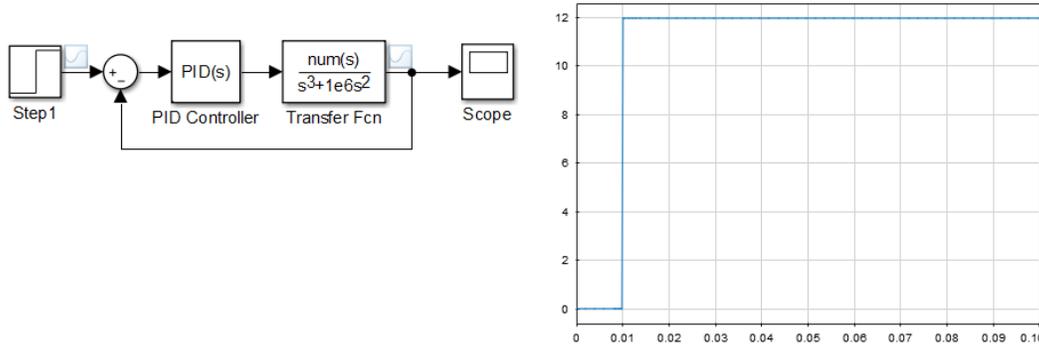
#### 4.2 CSC converter fed with PID controller

##### 4.2.1 Simulation results for CSC converter in ON state

The simulated transfer function for this state

$$G_{ON} = \frac{10^5 S^2 + 5.821 \times 10^{-6} S}{S^3 + 10^6 S^2}$$

The obtained PID parameters using GA are 894 ( $k_p$ ), 446 ( $k_i$ ), 1088 ( $k_d$ ). The transfer function and PID values were incorporated in the below Simulink block (fig. 6). The step function was fixed with maximum value of 12 and step time as 0.01 and simulated. The obtained graph is free from overshoot. The rise time and settling time are in the order of milli seconds.



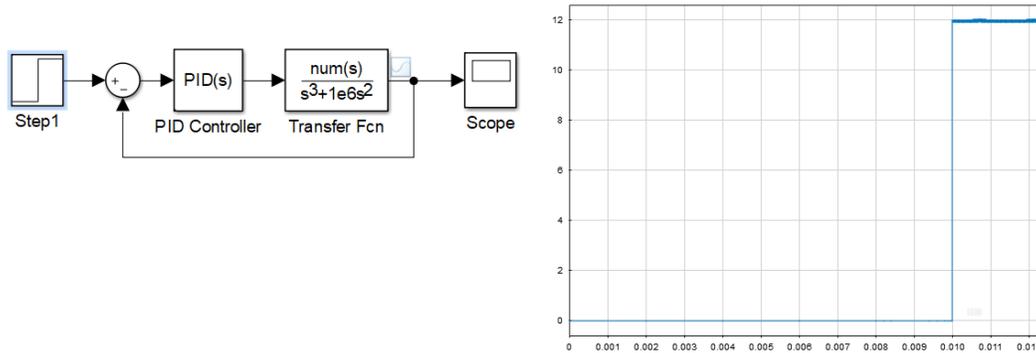
**Figure 5:** Closed loop CSC converter in ON state using Genetic algorithm based PID controller

##### 4.2.2 Simulation results for CSC converter in OFF state

The simulated transfer function for this state

$$G_{OFF} = \frac{10^5 S^2 + 5.821 \times 10^{-6} S + 2 \times 10^{12}}{S^3 + 10^6 S^2}$$

The obtained PID parameters using GA are 266 ( $k_p$ ), 1370 ( $k_i$ ), 2000 ( $k_d$ ). The transfer function and PID values were incorporated in the below Simulink block (fig. 8). The step function was fixed with maximum value of 12 and step size as 0.01 and simulated. The obtained graph is free from overshoot. The rise time and settling time are in the order of milli seconds.



**Figure 6:** Closed loop CSC converter in OFF state using Genetic algorithm based PID controller

## 5 Conclusion

The function of the CSC converter was identified and the circuit in ON and OFF states were recognized for modeling. The inputs ( $V_g$ , and  $i$ ), state variable ( $i_L$ ,  $V_{C1}$ , and  $V_{C2}$ ) and output ( $V_0$ ) were assigned to do state space model. State space model of CSC converter in ON and OFF states were developed using the Kirchhoff's laws. The state space matrices were derived to construct the transfer functions of ON & OFF states. The step response of the converter was simulated using MATLAB. The response curve corresponding to the circuit element values as shown in table 8, shows no overshoot and very high stable in nature. The step response curve was obtained and analyzed using different values of circuit elements and the optimized data was identified as rise time and settling time are  $4 \times 10^{-8}$  s and  $8 \times 10^{-8}$  s respectively, but the amplitude is very meager. The Genetic algorithm used PID controller was used to attain the expected amplitude for both ON and OFF states. The obtained PID parameters (894 ( $k_p$ ), 446 ( $k_i$ ), 1088 ( $k_d$ ) for ON state, and 266 ( $k_p$ ), 1370 ( $k_i$ ), 2000 ( $k_d$ ) for OFF state) were entered in the PID block in simulation of ON and OFF states. The obtained graphs are free from over shoot, rise and settling time in the order of 10 milli seconds, and the maximum amplitude (12) as expected in these cases. The excellent results of circuits characteristic parameters is very useful guideline for constructing such CSC converters for DC-DC conversions especially in maximum power point process in driving solar energy using solar panel.

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